

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

5 Applicant(s): Yi-Chang Wu et al.

Serial No.: 10/709,052

Filing Date: 4/8/2004

Examiner: MASKULINSKI MICHAEL C

Art Unit: 2113

Docket No.: WISP0045USA

10 Title: ERROR-EXAMINING METHOD FOR MONITOR CIRCUIT

To: Commissioner for Patents  
P.O. BOX 1450  
Alexandria, VA 22313-1450

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Subject: Status Inquiry of Request for transliteration error correction

Dear Sir,

20 The applicant submitted a request for transliteration error correction on 10/21/2005 to correct the second inventor's name from Yi-Hson Chen to Yi-Hsun Chen (Attachment 1). However, the applicant found that the second inventor's name of the above-mentioned application has not been corrected so far.

25 The undersigned agent hereby respectfully requests the second inventor's name to be corrected from Yi-Hson Chen to Yi-Hsun Chen as soon as possible. Your quick response will be greatly appreciated.

30

Respectfully submitted,

Winston Hsu

Date: 12/29/2006

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Note: Please leave a message in my voice mail if you need to talk to me. The time in D.C. is 13 hours behind the Taiwan time, i.e. 9 AM in D.C. = 10 PM in Taiwan)

10/709,052 ERROR-EXAMINING METHOD FOR MONITOR CIRCUIT

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